L Number	Hits	Search Text	DB	Time stamp
3	29	(cmos adj chip) and (led) and (len\$4) and (transparent) and (switch\$3)	USPAT	2003/10/08
4	812	biometric and security	USPAT	2003/10/08
5	26	340/5.53 and biometric same security	USPAT	2003/10/08
-	1	6,028,773.pn.	USPAT	2003/10/07
	2	non-volatile adj memory and pixel adj defect adj map	USPAT	2003/10/07
-	1	(cmos adj chip) and (transparent adj window)	USPAT	2003/10/07
-	53258	security and system	USPAT	2003/10/07
-	18	(transparent) and (switch\$3)) and	USPAT	2003/10/07 14:28
-	3	(security and system) mechanical adj switch same cmos same chip	USPAT	2003/10/07 14:32
-	9	electro same switch same cmos same chip	USPAT	2003/10/07
-	124	switch with cmos with chip	USPAT	2003/10/07
-	17	switch with activat\$4 with cmos and LED	USPAT	2003/10/07
-	1	6,111,977.pn.	USPAT	2003/10/07
-	239	biometric same security same system	USPAT	2003/10/07
-	276	electro-mechanical adj switch	USPAT	2003/10/07
-	5	electro-mechanical adj switch and security adj system	USPAT	2003/10/08
-	3	electro-mechanical adj switch and fingerprint\$3	USPAT	2003/10/07 15:42
-	21	electro-mechanical adj switch and security	USPAT	2003/10/07 15:58
-	1	capacitive adj switch and biometric	USPAT	2003/10/07
-	5	capacitive adj switch and biometric and system	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/10/07 16:04
-	17	capacitive adj switch and security and system	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/10/08 13:57
-	1	4,801,935.pn.	IBM_TDB USPAT	2003/10/08 16:02